

Figure 2

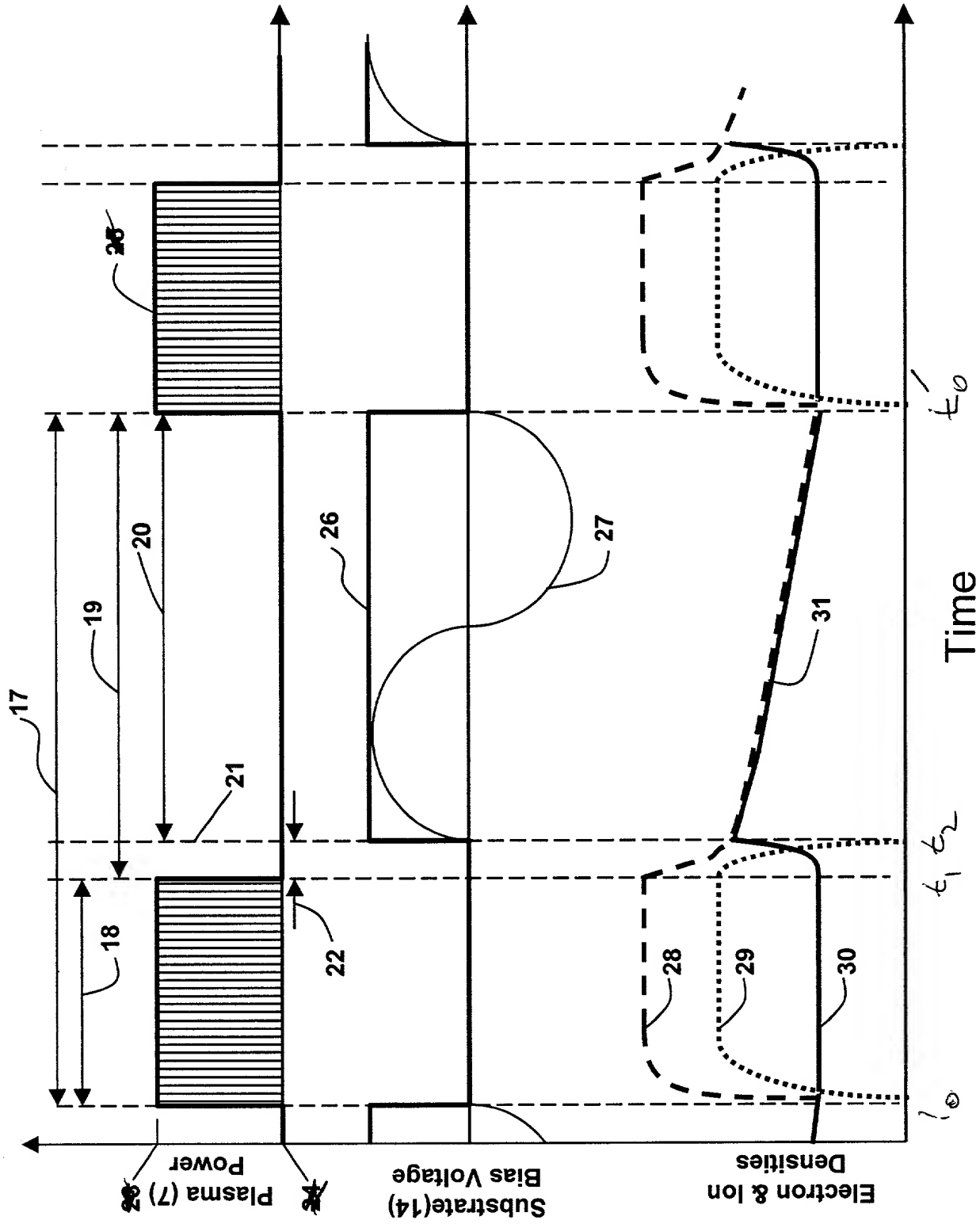


Figure 3

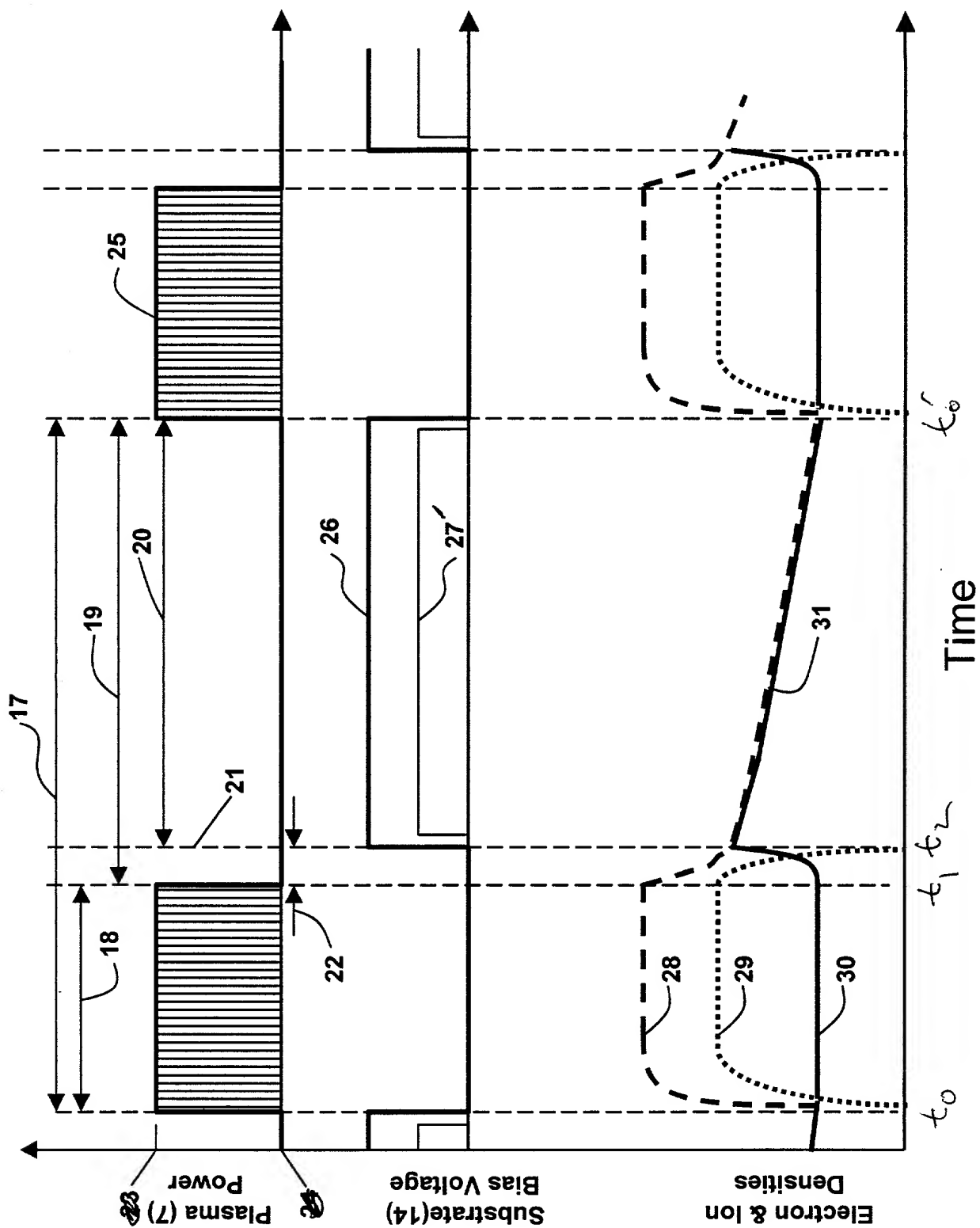


Figure 4

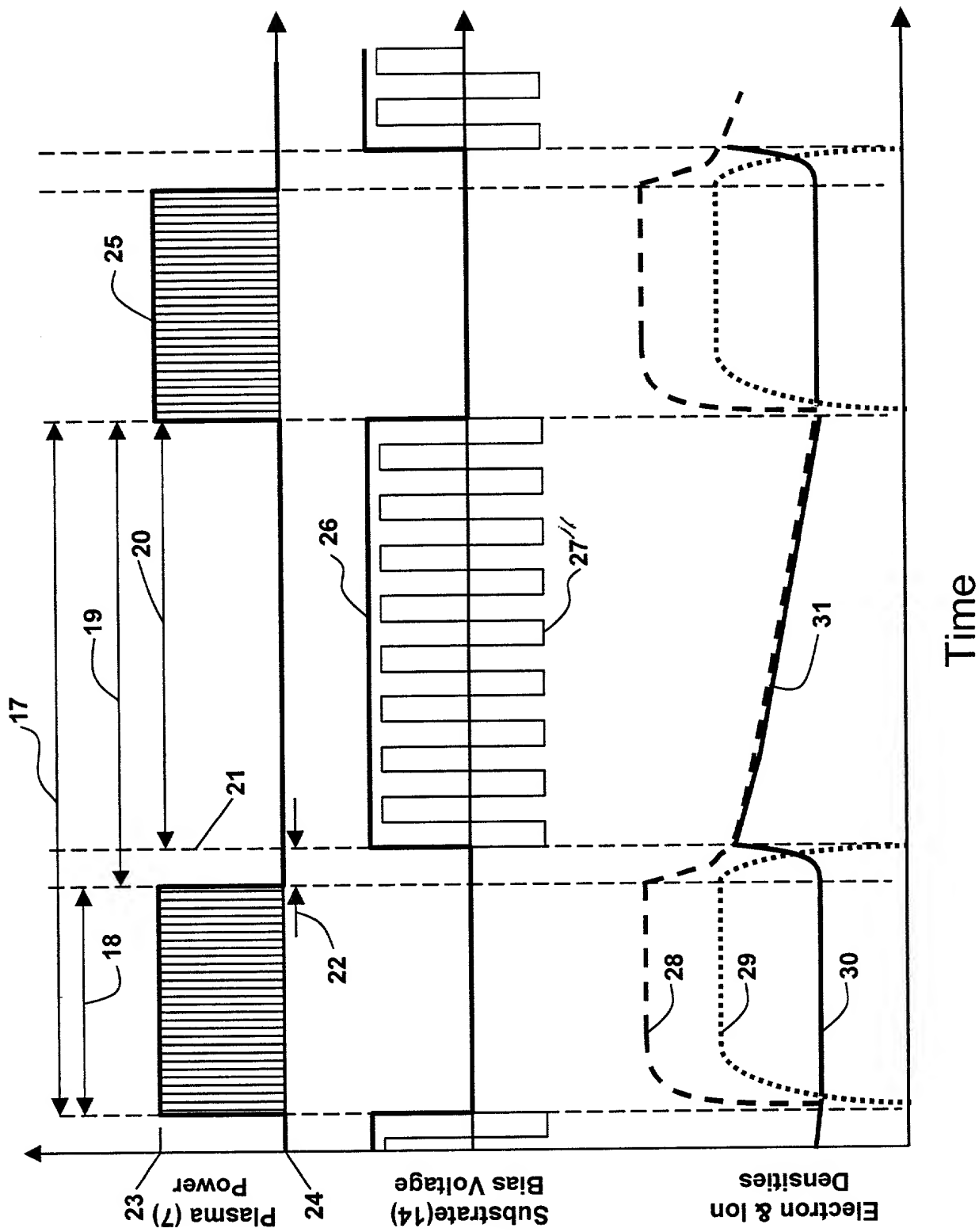


Figure 5

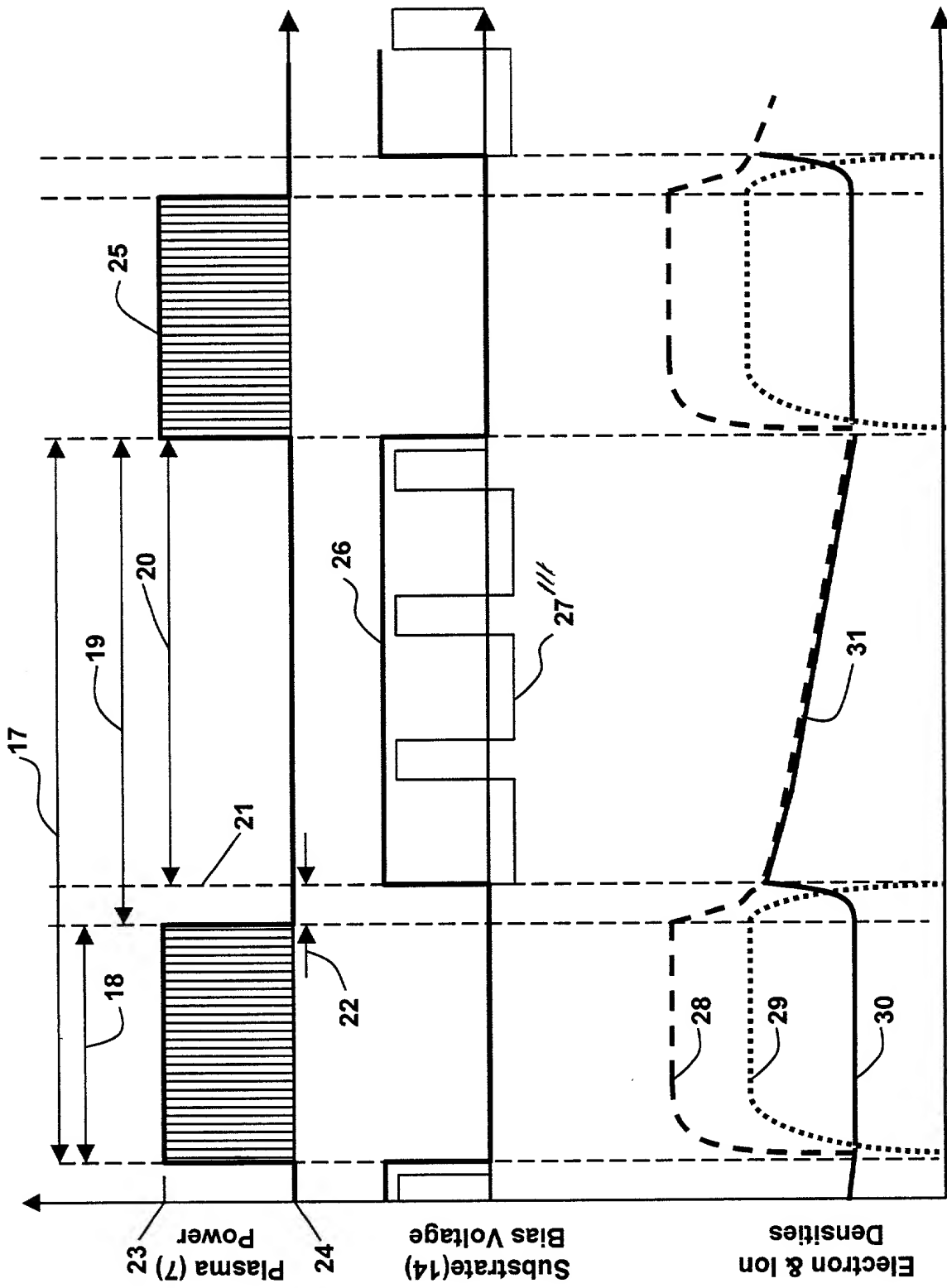


Figure 6

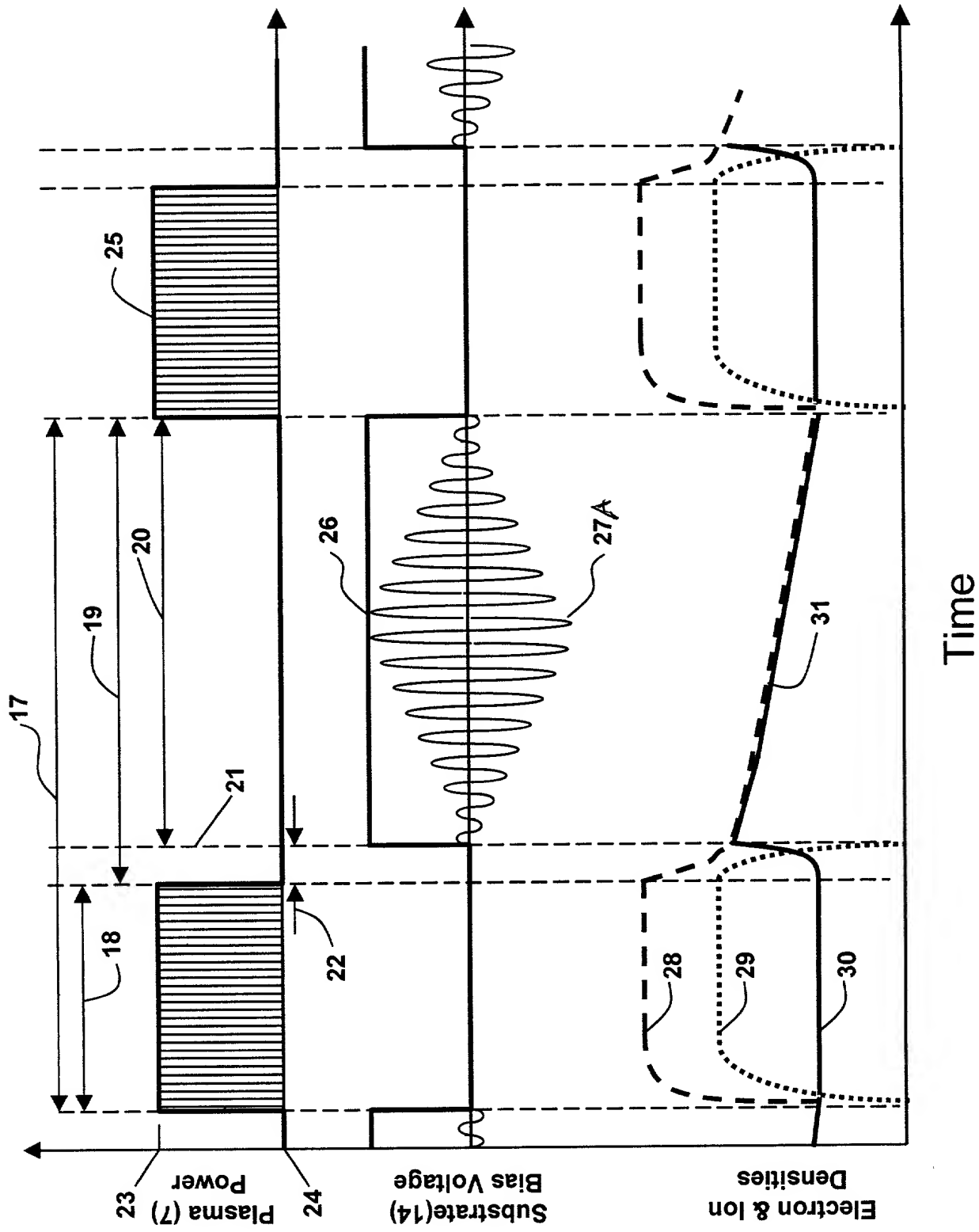


Figure 7

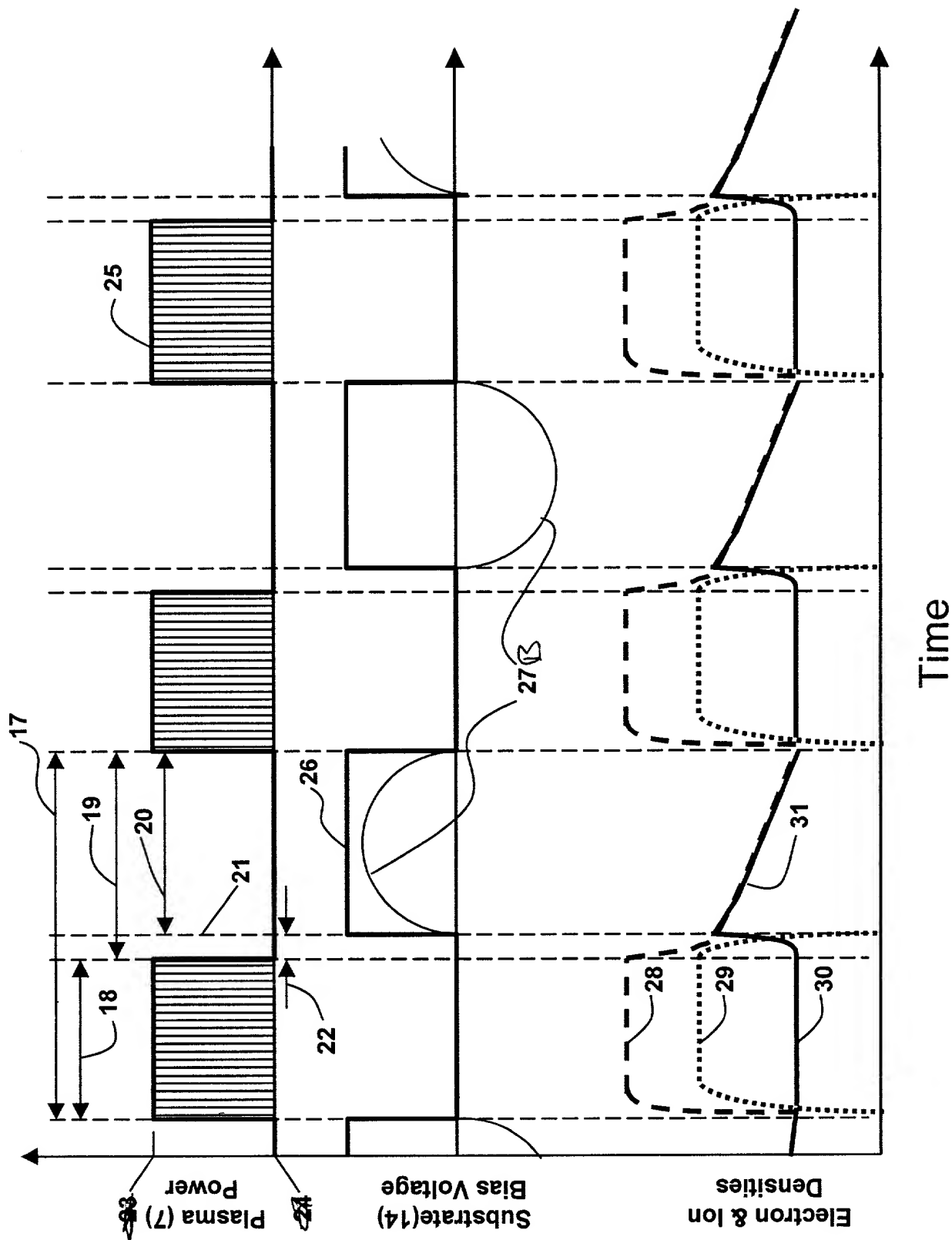


Figure 8



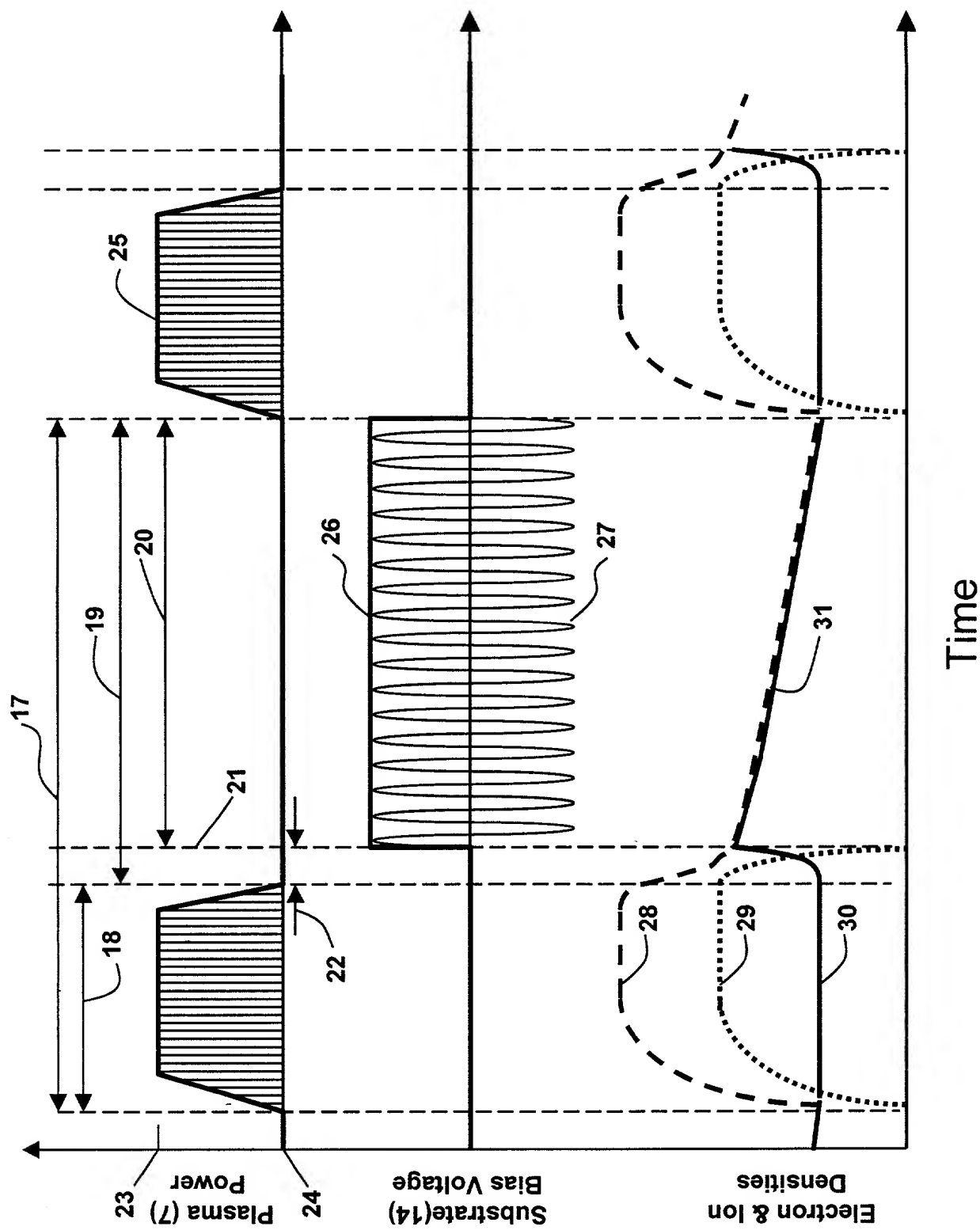


Figure 9



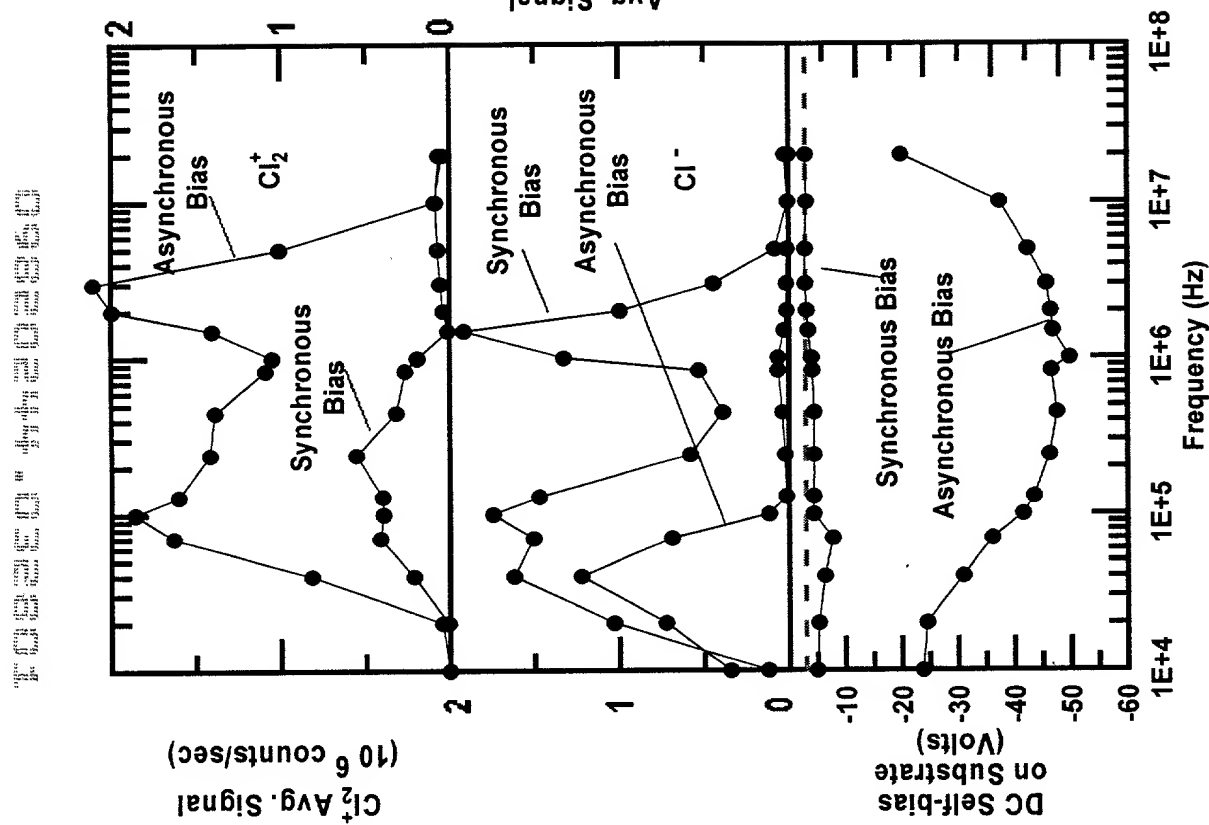


Figure 11: Testing results for a specific embodiment of the invented technique.

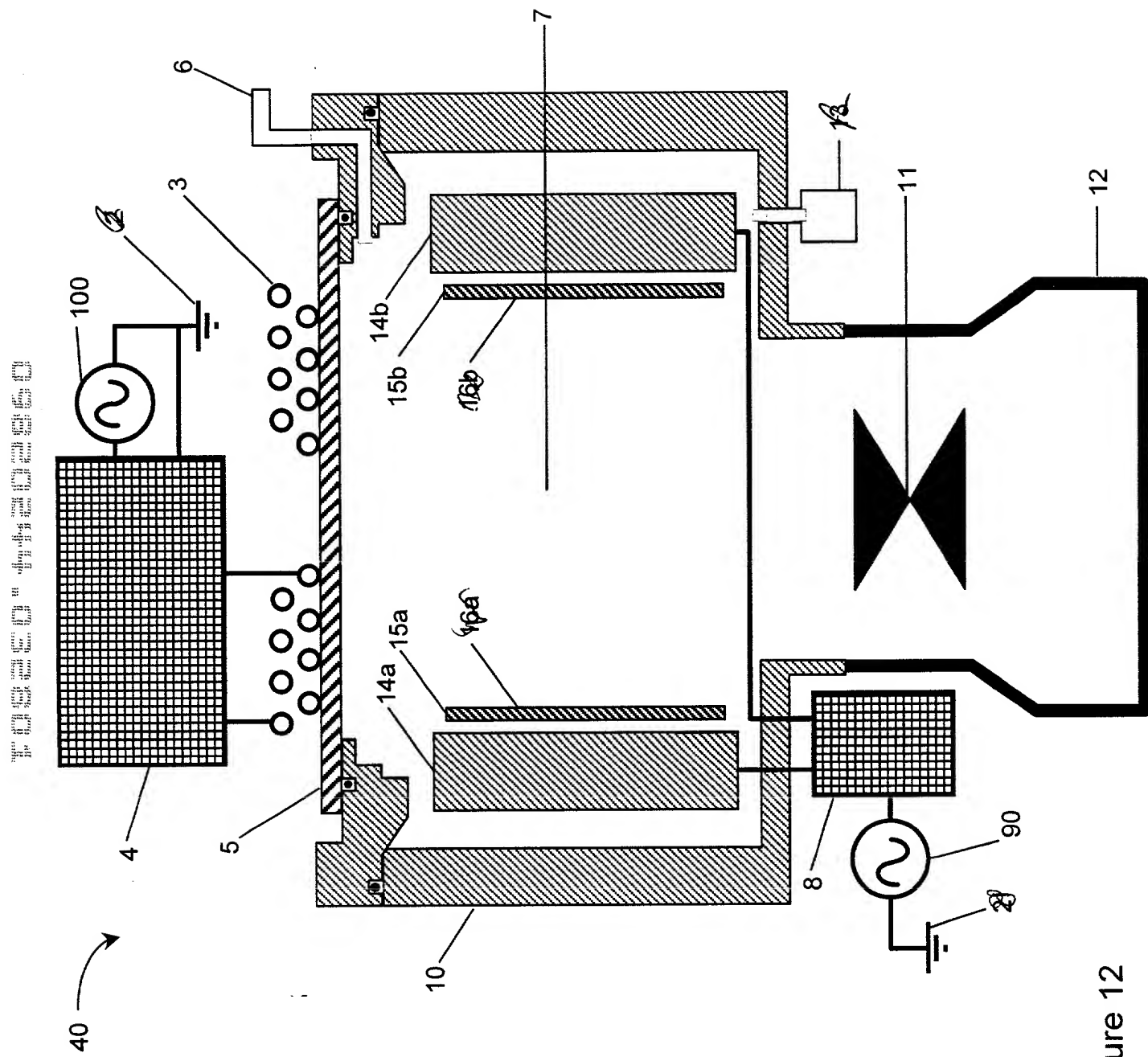


Figure 12

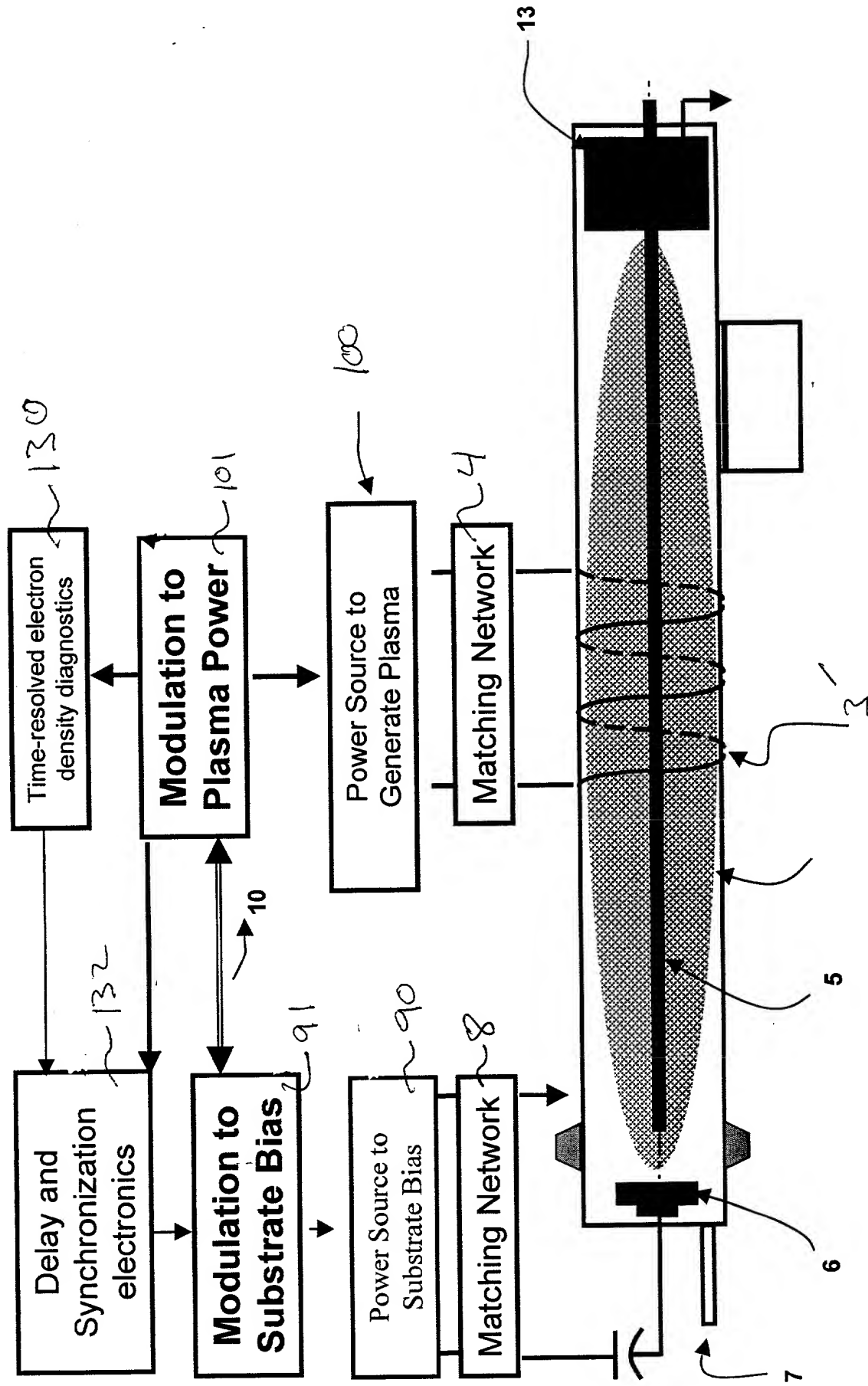


Figure 13 Plasma Reactor and Bias Synchronization schematics of a specific embodiment of the invention.

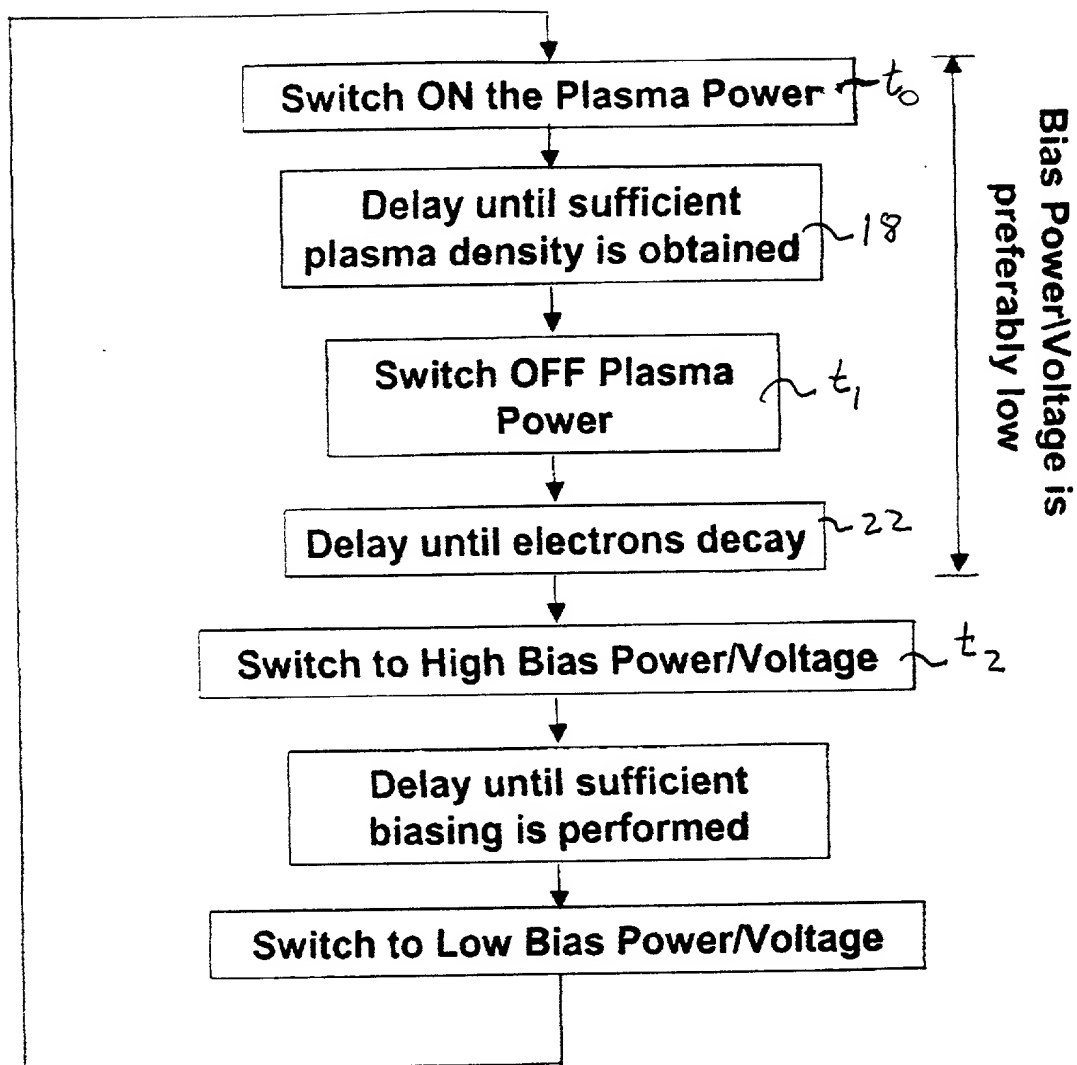


Figure 14